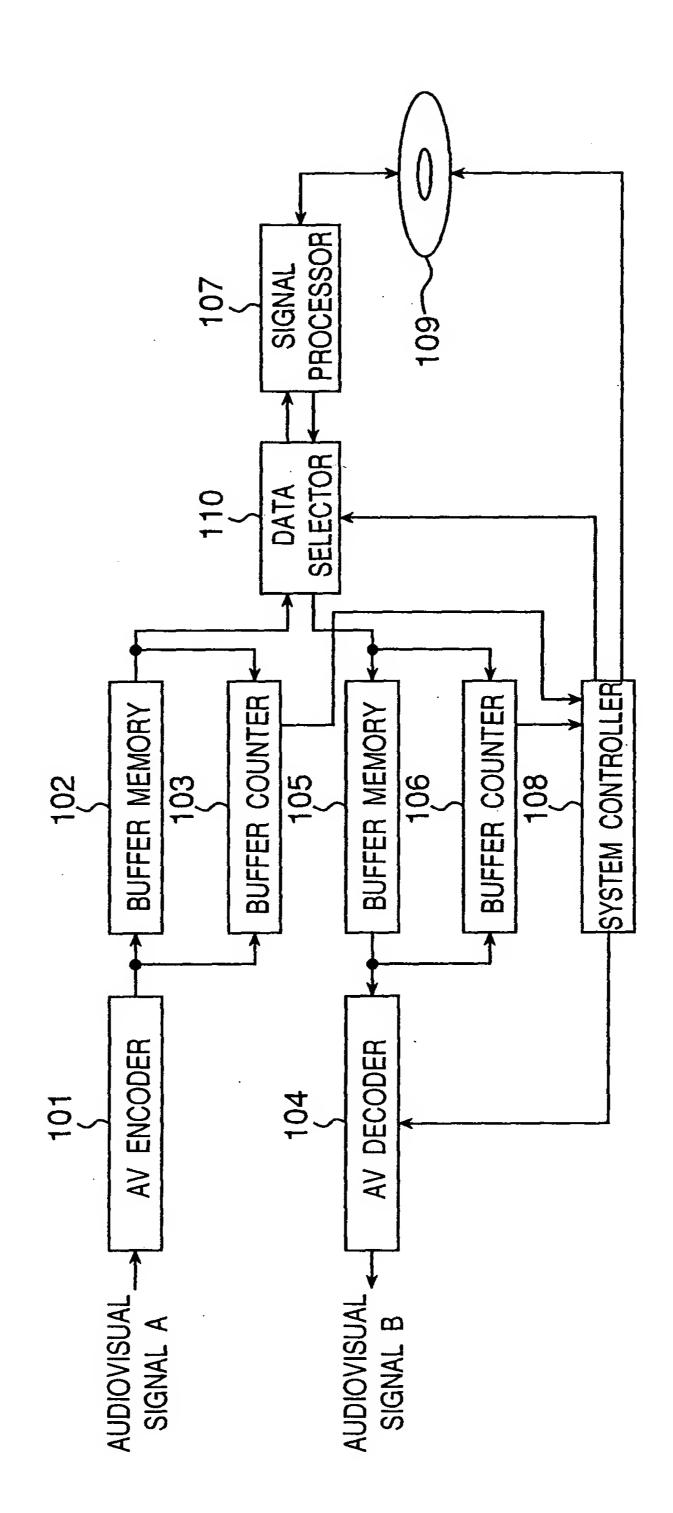
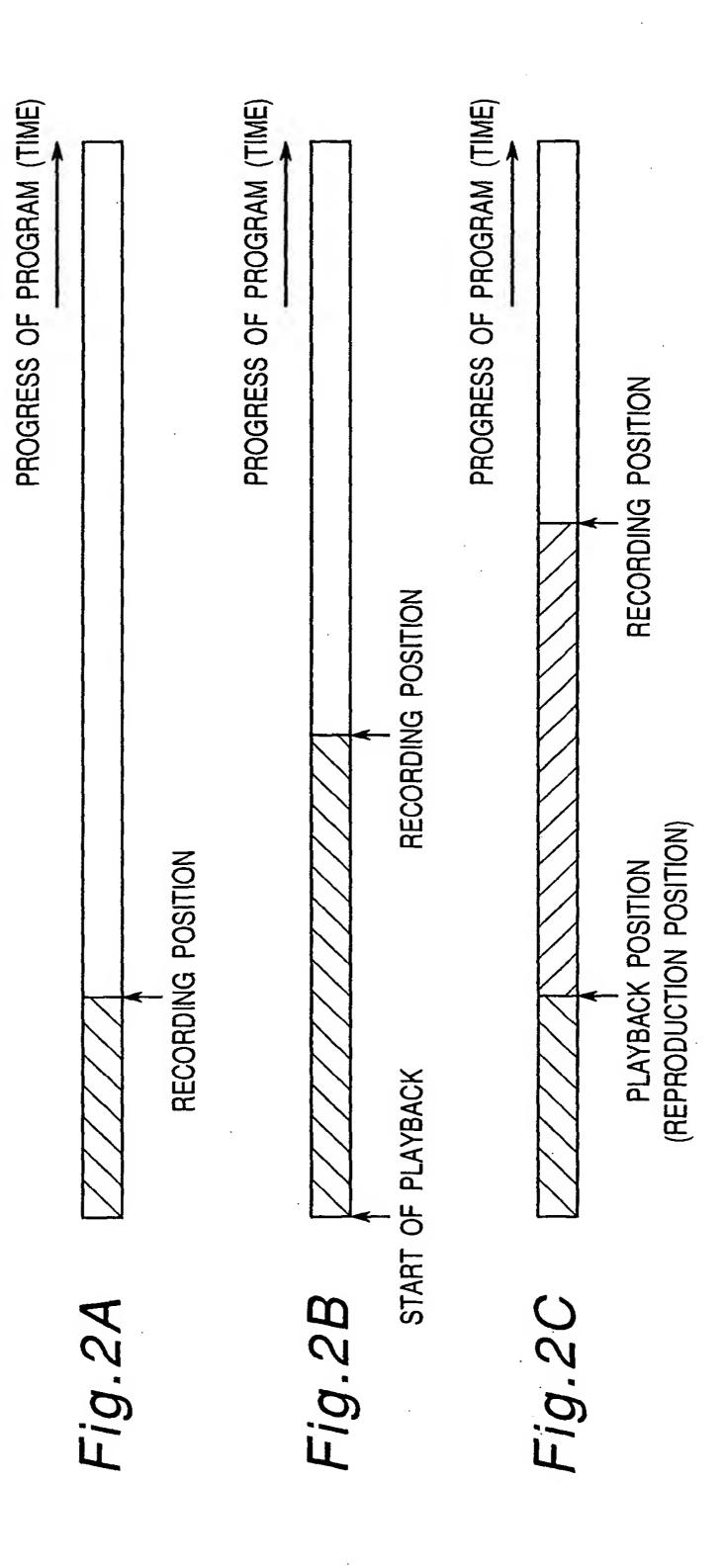
Fig. 1



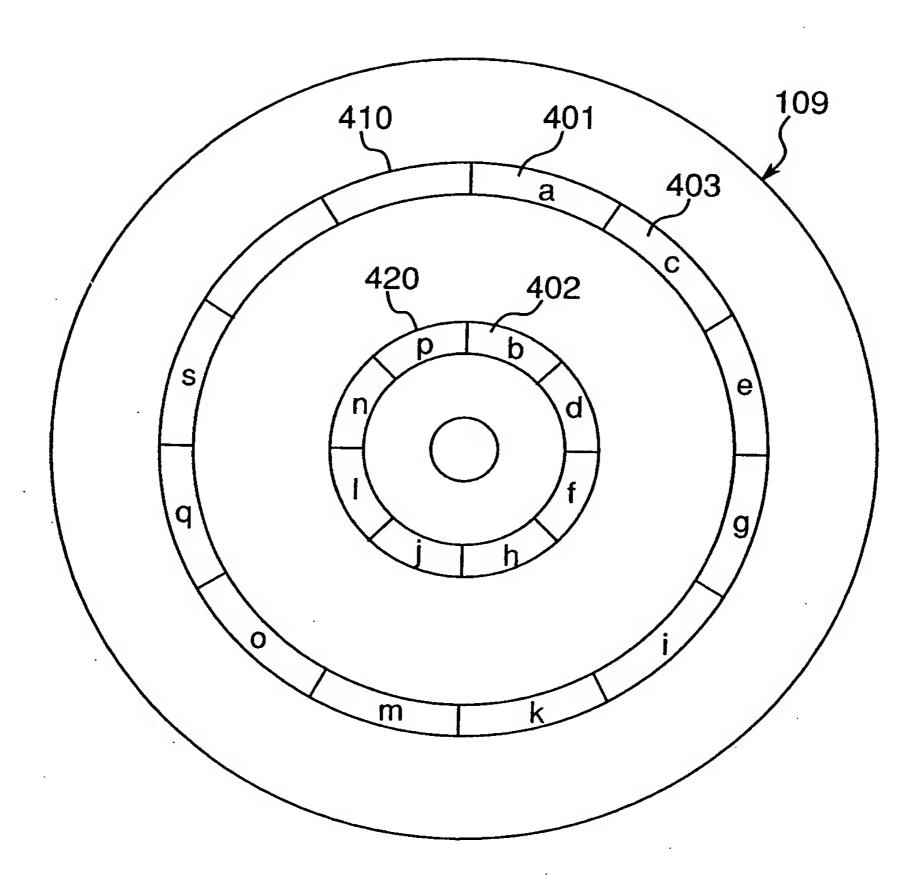


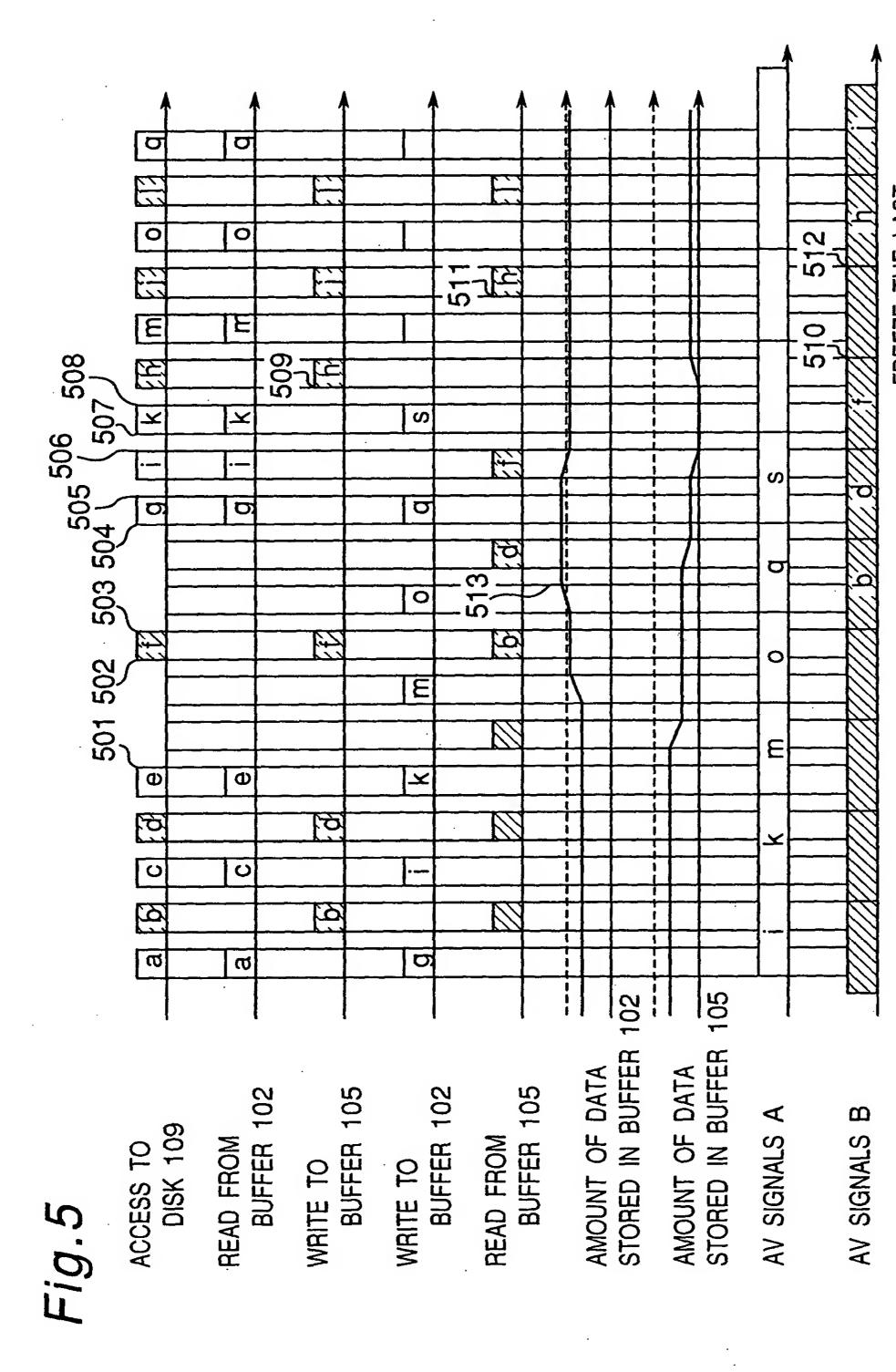
d × D 0 12 9 E D g Φ Φ k Ö Q 302 ပ ပ Ö ർ READ FROM BUFFER 105 READ FROM BUFFER 102 WRITE TO BUFFER 105 WRITE TO BUFFER 102 ACCESS TO DISK 109 Fig.3

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Fig.4





FREEZE THE LAST FRAME OF f

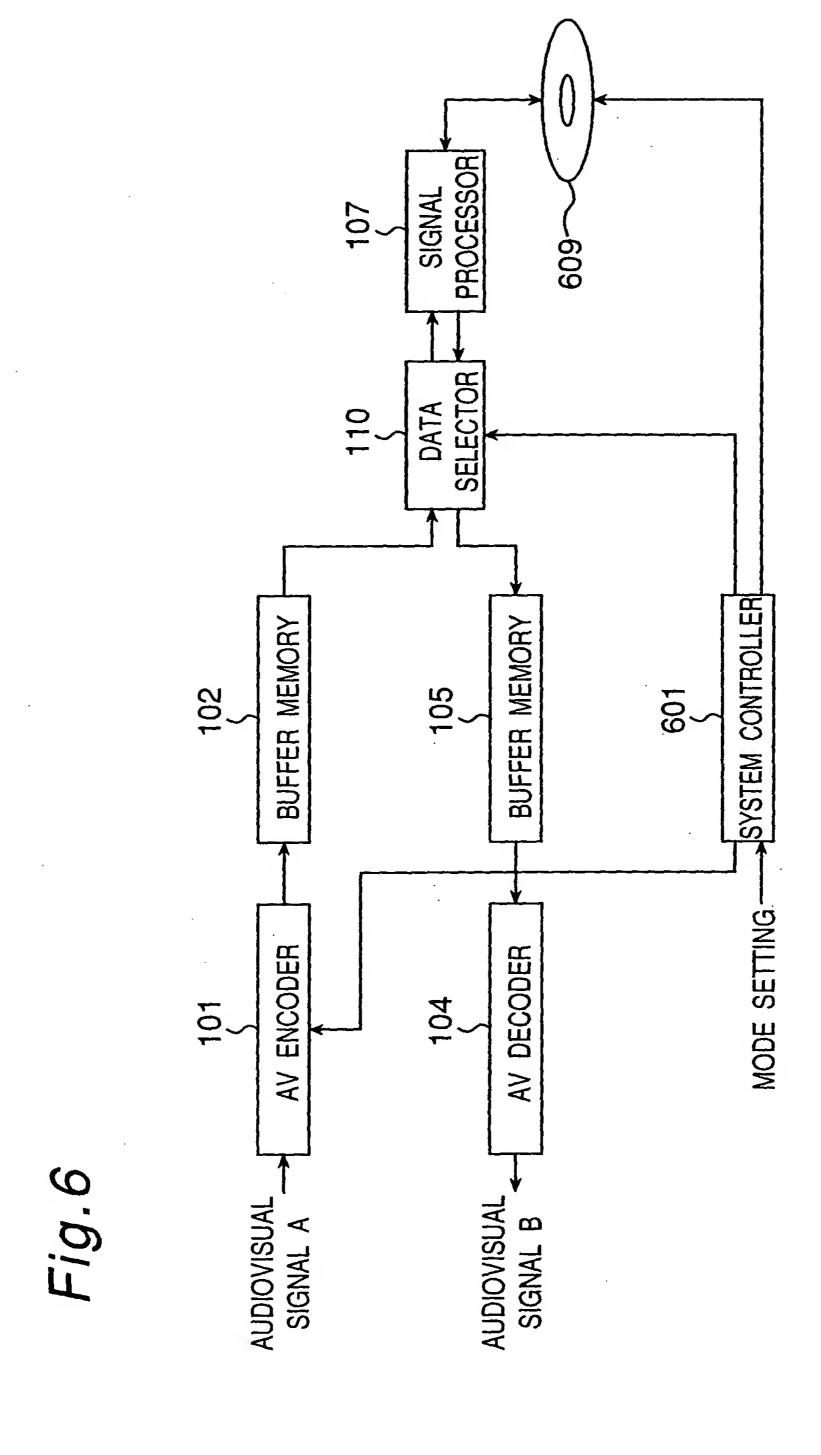


Fig. 7

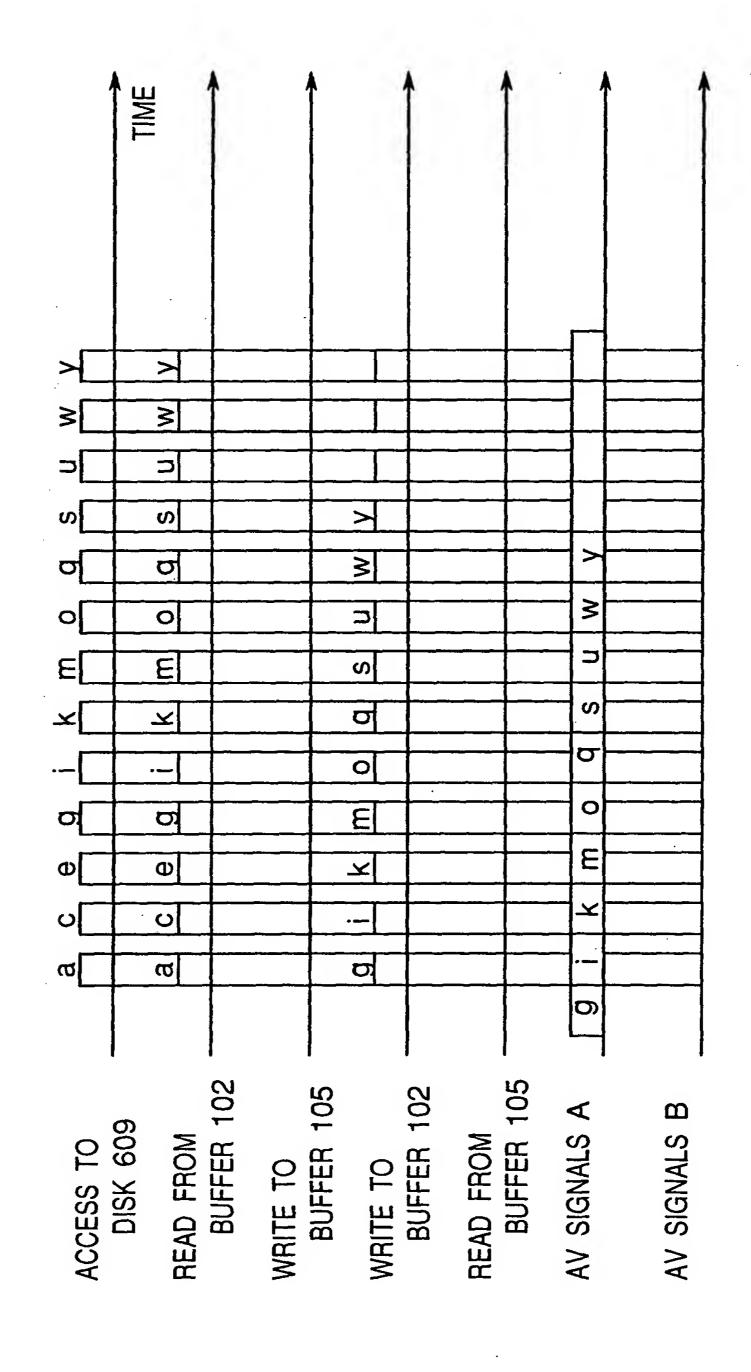
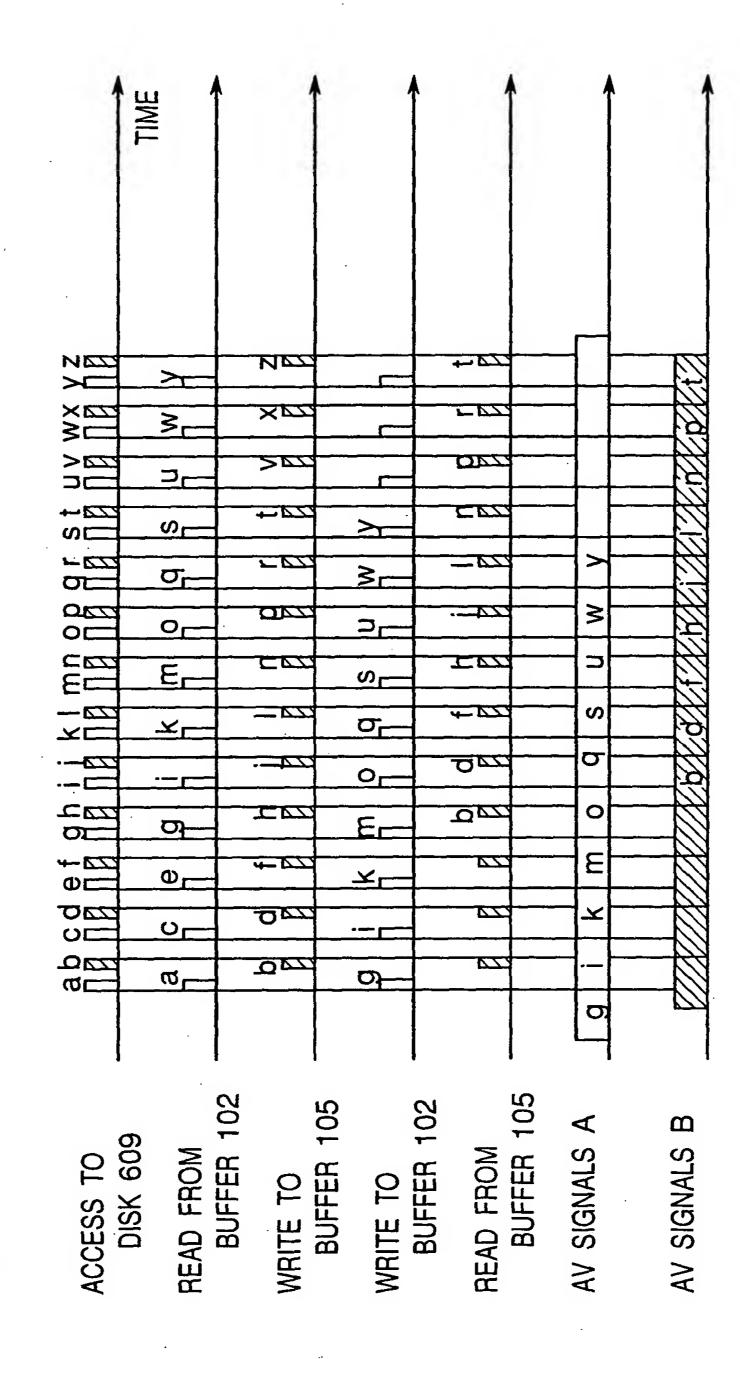


Fig.8



BUFFER MEMORY 102 7 902 AV ENCODER Fig.9 AUDIOVISUAL SIGNAL A

SIGNAL PROCESSOR 606 DATA SELECTOR SYSTEM CONTROLLER BUFFER MEMORY 901 REPRODUCTION CONTROL -AV DECODER AUDIOVISUAL SIGNAL B

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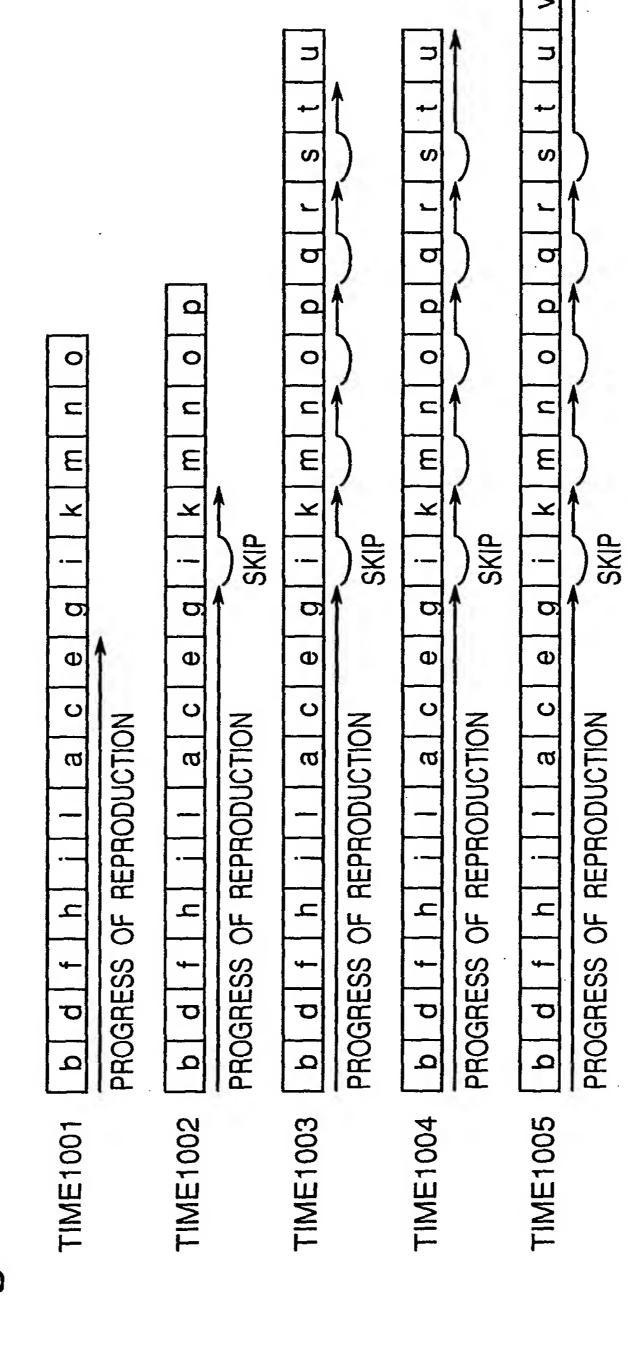
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Fig. 11



PROCESSOR SIGNAL 909-SELECTOR DATA SYSTEM CONTROLLER BUFFER COUNTER BUFFER COUNTER BUFFER MEMORY BUFFER MEMORY DISPLAY GENERATOR 1201 106 105 103 AV ENCODER AV DECODER AV SIGNAL SUPERPOSER 1203 Fig. 12 AUDIOVISUAL SIGNAL A AUDIOVISUAL SIGNAL B

Fig. 13

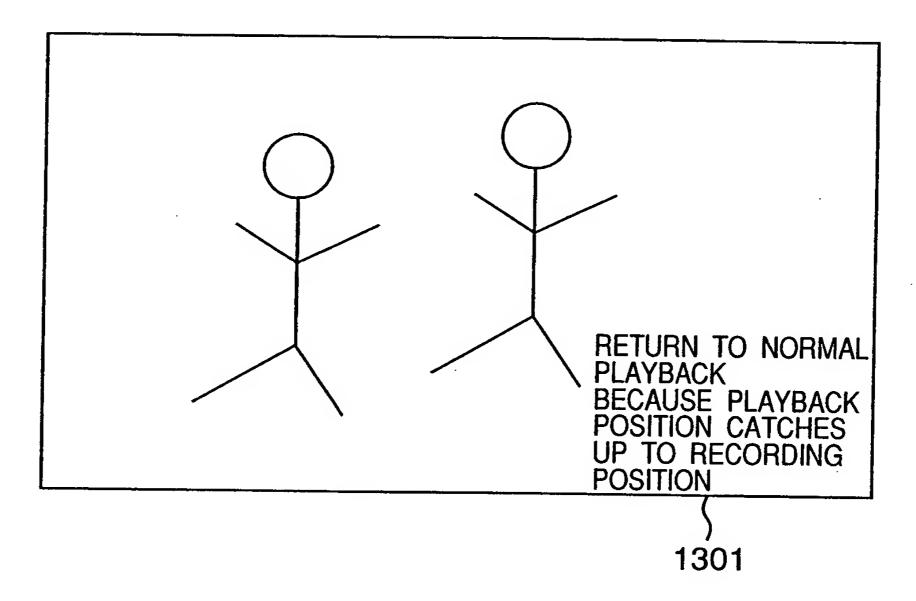


Fig. 14

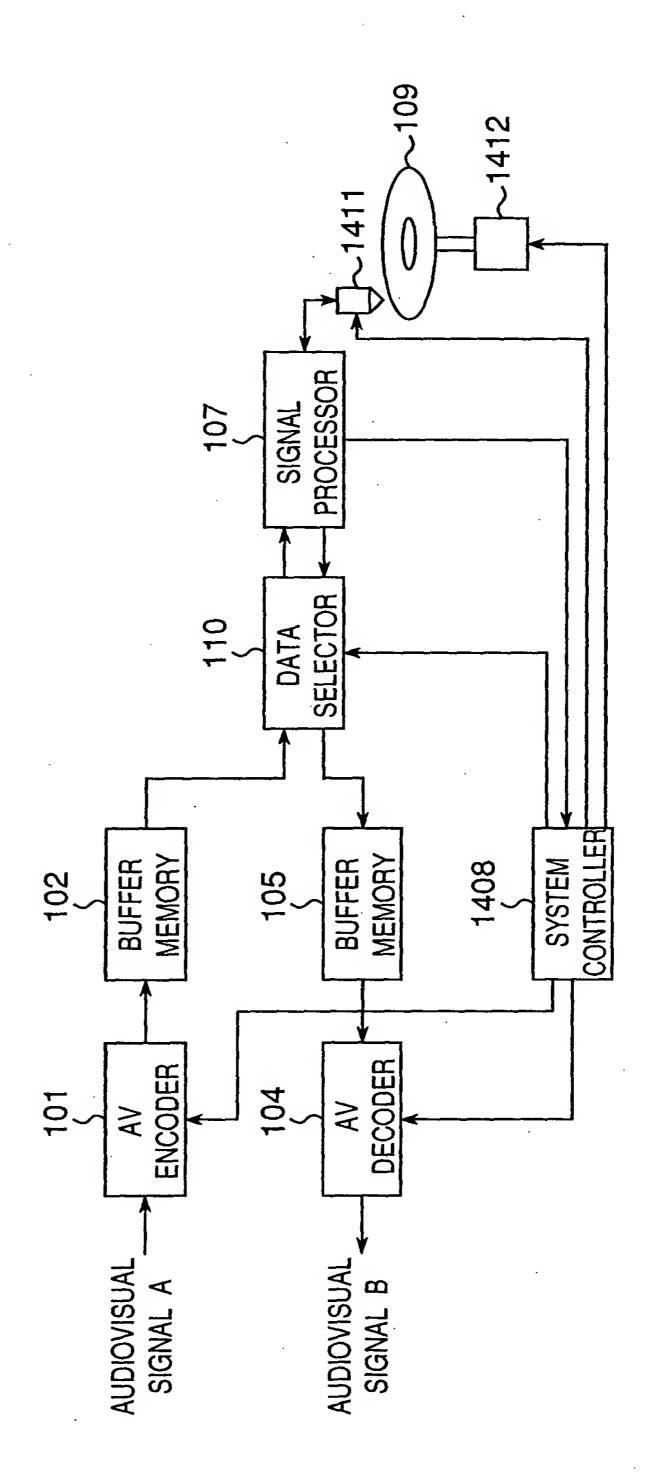


Fig.15

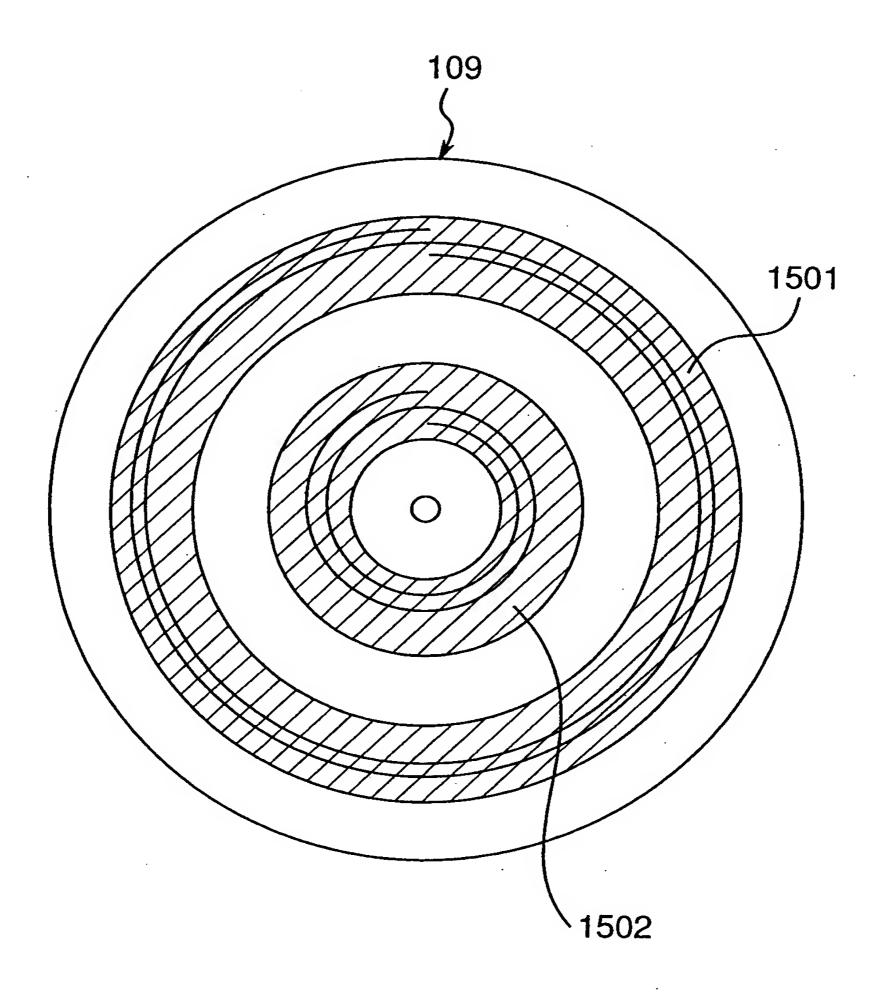
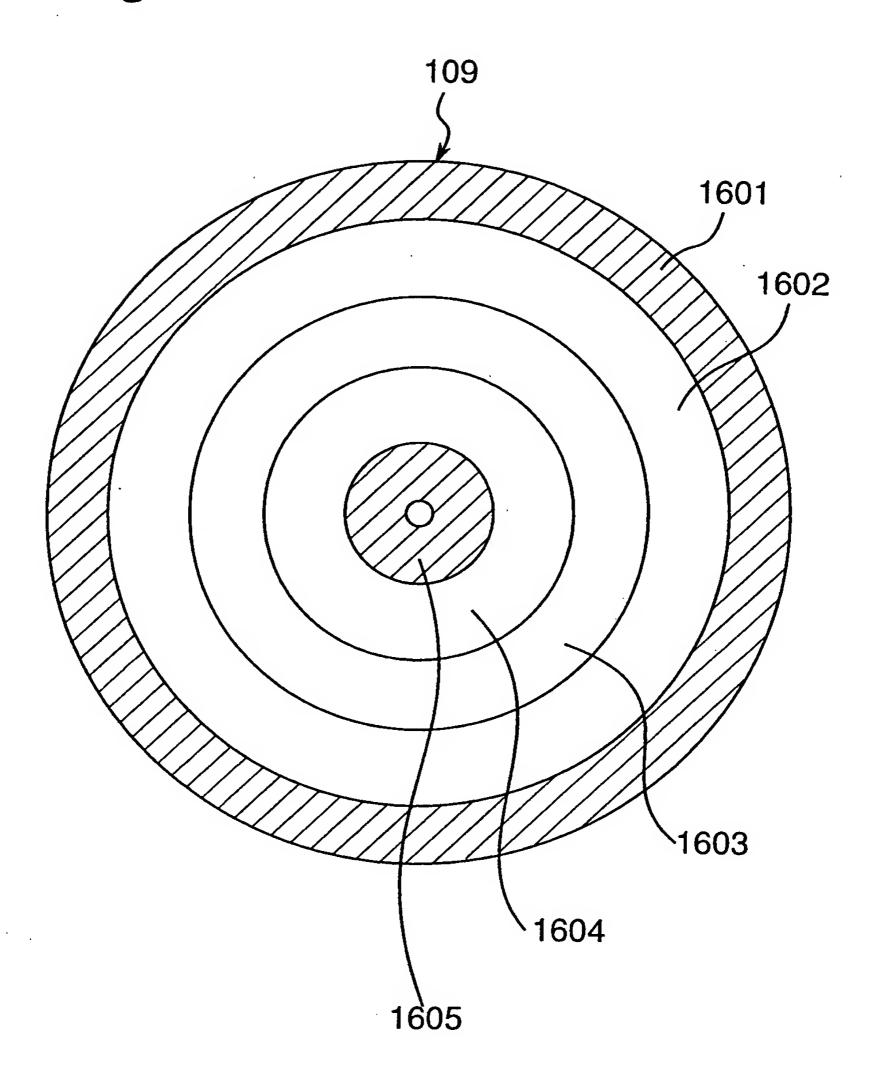


Fig. 16



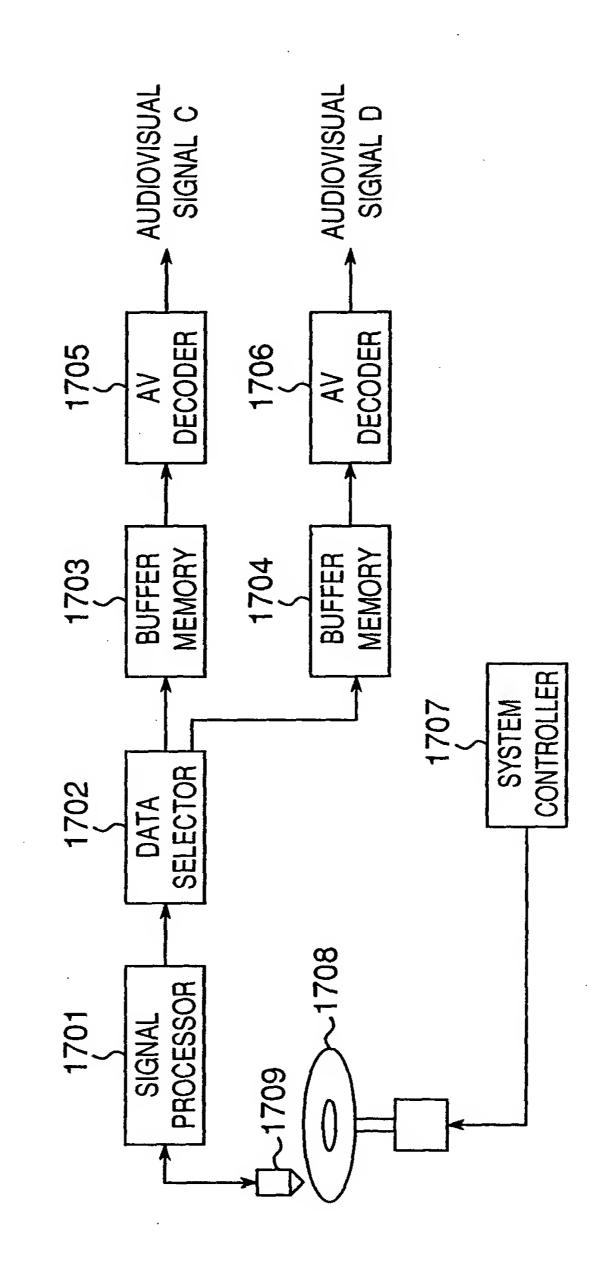


Fig. 18

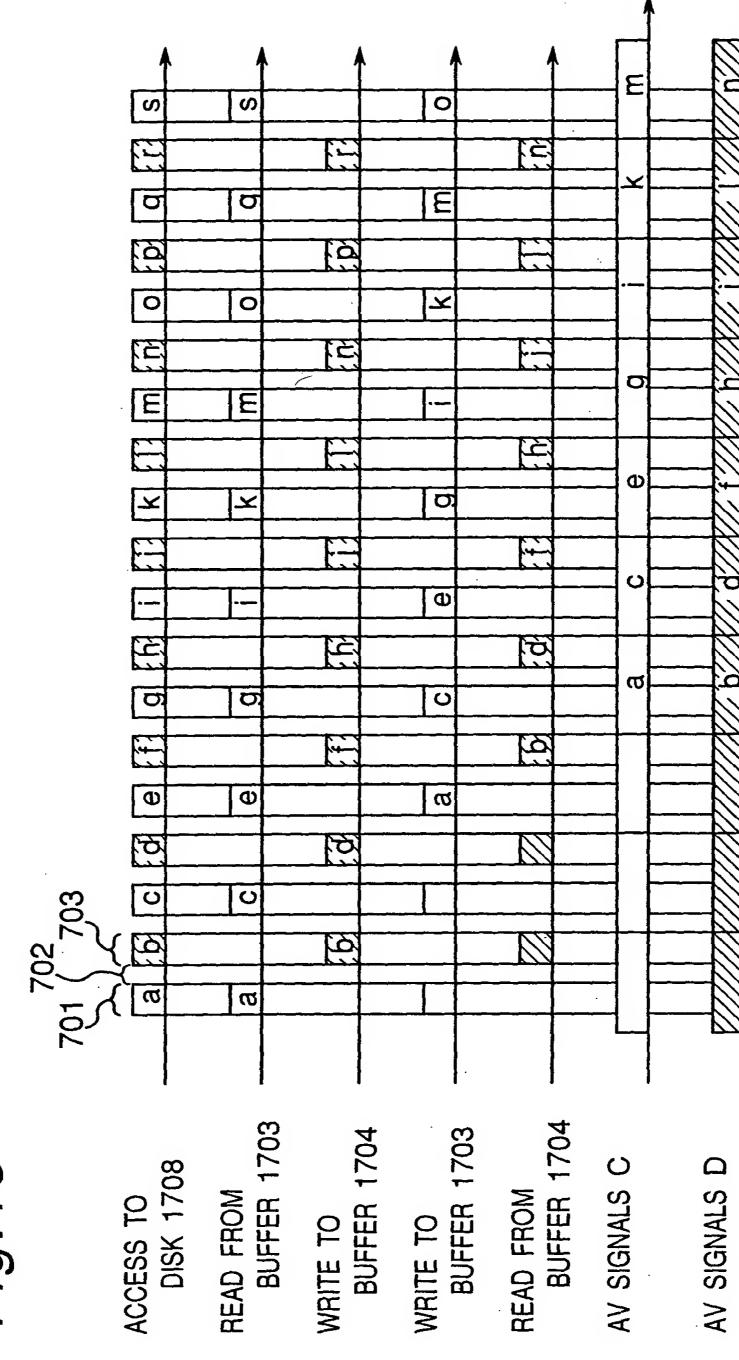


Fig.19

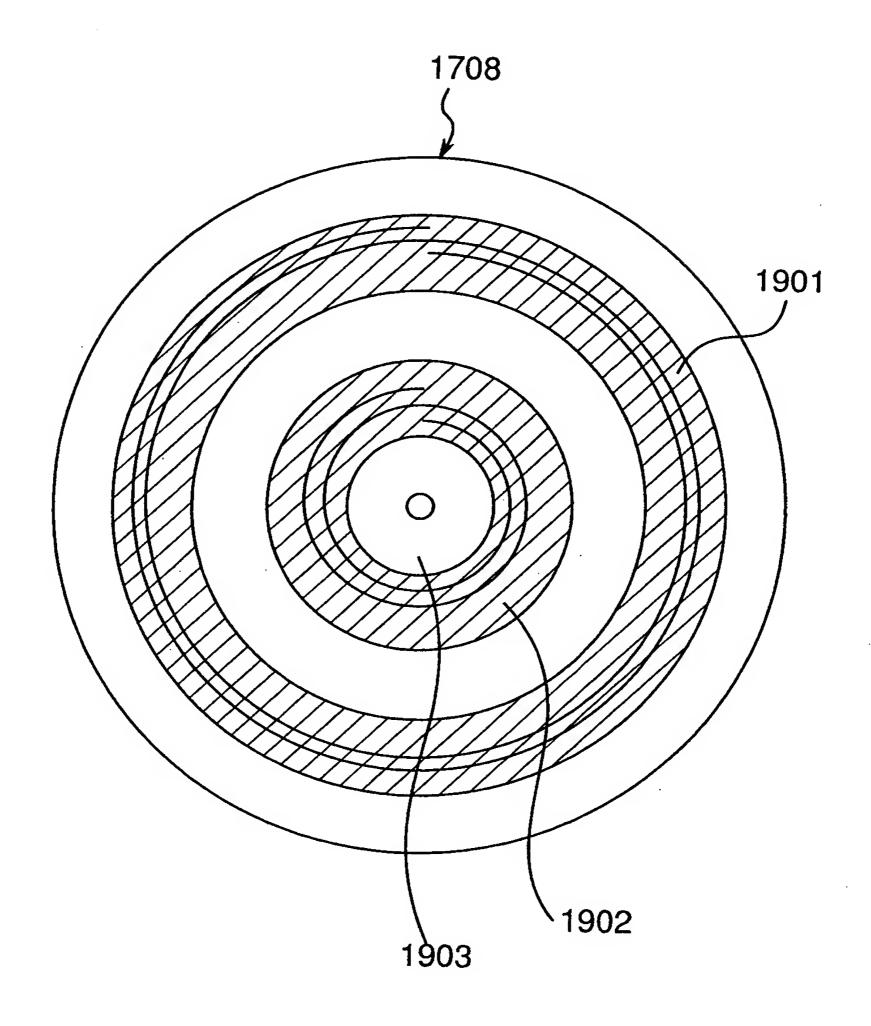


Fig. 20A DATA STREAM OF AV SIGNAL C

FIG. 20B DATA STREAM OF AV SIGNAL D